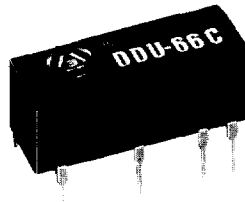


HCMOS — Logic

# Digital Delay Units

SERIES: **DDU-66C**

**5 Outputs**  
**14 pin DIP**



**Features:**

- Automatic insertable.
- Low Cost.

**Specifications:**

- No. Taps: 5 equally spaced taps.
- Total delay tolerance:  $\pm 5\%$  or 2 ns whichever is greater.
- Rise time: 8 ns typ.
- Temperature coefficient: 300 PPM/°C.
- Temperature range: 0° C to + 70° C.
- Supply Voltage: 5 Vdc  $\pm 5\%$ .
- Supply current:
  - I<sub>CCN</sub> = 10 ma
  - I<sub>CCL</sub> = 40  $\mu$ a
- Fan-out: 10 LSTTL loads min.
- DC parameters: See HCMOS-Logic Table on Page 6.

Part No. Table

DDU-66C (Standard)	Total Delay (ns)	Tap Delay (ns)
*DDU-66C-10	10 $\pm$ 2	2.5 $\pm$ 1
*DDU-66C-20	20 $\pm$ 2	5 $\pm$ 2
*DDU-66C-30	24 $\pm$ 2	6 $\pm$ 2
DDU-66C-50	50 $\pm$ 3	10 $\pm$ 3
DDU-66C-60	60 $\pm$ 3	12 $\pm$ 3
DDU-66C-75	75 $\pm$ 4	15 $\pm$ 3
DDU-66C-100	100 $\pm$ 5	20 $\pm$ 3
DDU-66C-125	125 $\pm$ 6.5	25 $\pm$ 3
DDU-66C-150	150 $\pm$ 7.5	30 $\pm$ 3
DDU-66C-175	175 $\pm$ 8	35 $\pm$ 4
DDU-66C-200	200 $\pm$ 10	40 $\pm$ 4
DDU-66C-250	250 $\pm$ 12.5	50 $\pm$ 5

\*Time delay referenced to 1st tap.  
8 ns  $\pm$  2 ns inherent delay.

